



Shrithya Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IQAC

Internal Quality Assurance Cell



IQAC Initiative

“3-Days Workshop on Recent trends in Design of Digital System using EDA Tools”

22/06/23 to 24/06/23

Resource Person/s

Dr. Srinivasarao Udara

Trainer, JVT Hub,

Ranebennur

Deepika.v.B

Veena

Event coordinators

Ms. Deepika V B / Mrs. Veena H T
Assistant Professor

Dr. Praveen J

Verified By

Dr. Praveen J
Head of the Department
DAVANGERE-577 006

Dr. Sunil Kumar B S

Checked by

Dr. Sunil Kumar B S
Dean Academics

DEAN ACADEMICS
G.M. Institute Of Technology
DAVANGERE-577006.

Dr. Praveen J

Checked by

Dr. Praveen J
IQAC-Director
GM Institute of Technology
Davangere - 577 006.

Dr. Sanjay Pande M B

Approved by

Dr. Sanjay Pande M B

Principal
PRINCIPAL

GM Institute of Technology
Davangere - 577 006.



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Department of Electronics and Communication Engineering

Invitation

We cordially invite you for the

IQAC Initiative

“3-Days Workshop on Recent trends in Design of Digital System using EDA Tools”

in association with BJV Tech Hub.



22nd to 24th June 2023

Convener:

Dr. Praveen J
IQAC-Director
Professor & Head
Dept. of ECE, GMIT

President:

Dr. Sanjay Pande M B
Principal
GMIT, Davangere

Resource Person

Dr. Srinivasarao Udara
Trainer
Ranebennur

Coordinators

Ms. Deepika V B
Assistant Professor
Dept. of ECE, GMIT

Mrs. Veena H T
Assistant Professor
Dept. of ECE, GMIT

Organized By:

Department of Electronics and Communication Engineering
Venue: VLSI Lab, ECE Department.

Date: 22nd June 2023 Time: 9:30 AM

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Report On “3-Days workshop on Recent trends in Design of Digital System using EDA Tools”

The Department of Electronics and Communication Engineering, GM Institute of Technology, Davanagere organized a Technical Workshop on “3-Days workshop on Recent trends in Design of Digital System using EDA Tools” by the resource person **Dr. Srinivasarao Udara**, BJV tech hub, Ranebennur from **22nd June 2023 to 24th June 2023** at 9.30 am to 5.00 pm in VLSI Lab.

The workshop was inaugurated on 22nd June 2023 at 9:00AM. The resource persons, **Dr. Srinivasarao Udara**, BJV tech hub, Ranebennur **Dr. Sanjay Pande M B**, Principal, GMIT, **Dr. Praveen J**, HOD, ECE Department, the Event Coordinators **Ms. Deepika V B** and **Mrs. Veena H T** faculties and registered students of workshop were present in the function.

The Principal and HOD addressed all the students and informed to benefit from the workshop and use in real time applications.

Dr. Praveen J, Professor & Head, ECE department, was presided over the Workshop and welcomed the gathering and also briefed the assembled students about the importance of VLSI in present technologies. The faculty members of ECE and the students of ECE were present in the Workshop.

The key note speaker of the Workshop **Dr. Srinivasarao Udara**, started the presentation by giving introduction about the VLSI, usage and scope of Cadence tool in Academics as well as in Industry. He also gave technical importance of Cadence tool, he also conducted Hands on Session for designing and simulating digital circuit using Verilog HDL, Synthesis process of Digital circuits, Evaluation of synthesis reports to obtain optimum gate level Net List, design and simulate basic CMOS circuit and performed RTL-GDSII flow.

The resource persons encouraged all the students by giving awareness about importance of Cadence Virtuoso Platform in VLSI research field in both Academics and Industries to build their Career. The students were motivated and benefited.

Head of the Department

Coordinators

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Figure 1: Hands –on Session on Cadence Tool



Figure 2: Theory session by the resource person



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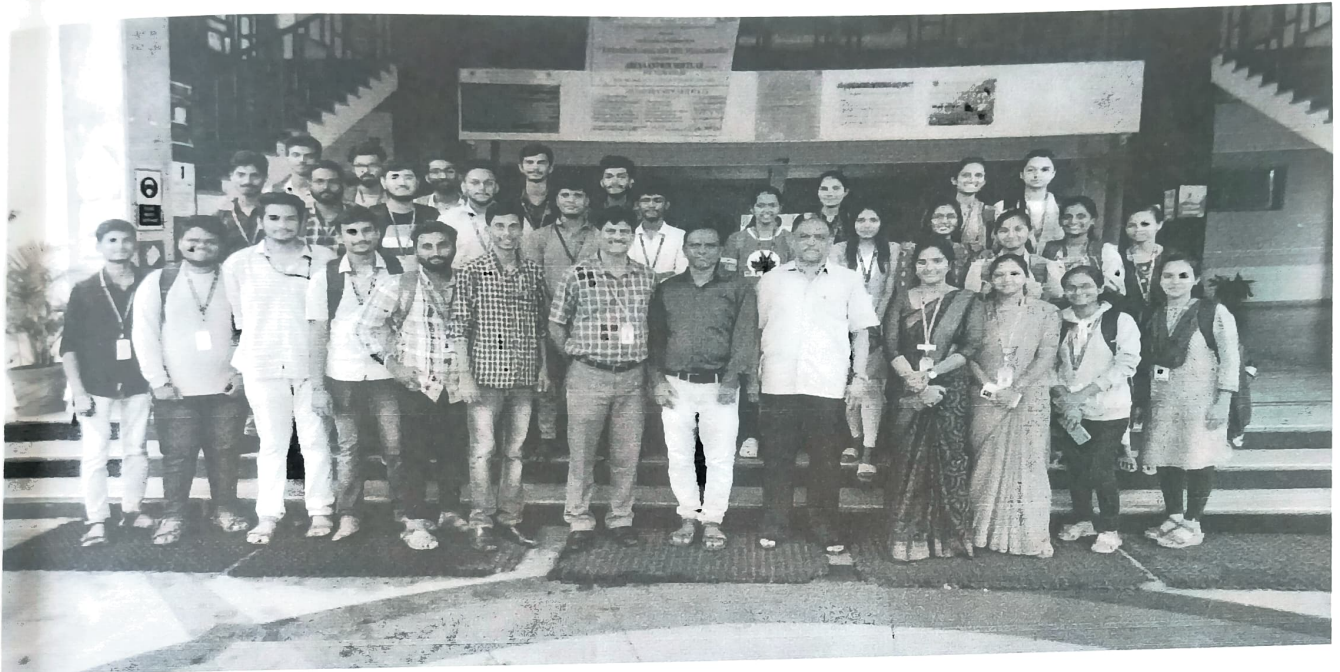


Figure 3: Workshop Attended Batch-2 students team



Figure 4: Felicitation to the Resource person



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GM INSTITUTE OF TECHNOLOGY

P. B. Road, Davanagere - 577 006, Karnataka, INDIA

IQAC Initiative
Department of Electronics & Communication Engineering
In association with BJV Tech Hub.



CERTIFICATE OF PARTICIPATION

This is to Certify that Mr. /Ms. _____ has
Participated in a 3-Days workshop on “**Recent Trends in Design of Digital System using EDA Tools**” from
15th to 17th of June 2023 organized by Department of Communication Engineering, GM Institute of
Technology, Davanagere.

Dr. Srinivasarao Udara
Trainer, BJV, Tech Hub.

Dr. Praveen J
Convenor

Dr. Sanjay Pande M B
Principal